



PROGRAMME DOSSIER • COMPUTING ARCHITECTURE • CAPABILITY AND STATUS

Not a new artificial intelligence model,
but a proposal to reinvent the computing substrate
that AI itself runs on.

The mathematical proof is complete.

Training and field validation are still to be done:
at system level the project sits at TRL

3 → 4



PRELIMINARY

Purpose and status of this document

This dossier exists to explain, accurately and without embellishment, what R. Rockefeller S.C. is building, what the group is capable of, and where the programme currently stands. Its purpose is to establish the technical and institutional basis for a specialist relationship with a single strategic partner inside the NATO and allied perimeter.

It is not an invitation to invest. It is not addressed to investors, it neither offers nor solicits any security, token, instrument or participation of any kind, and it asks no one to commit capital. No offer is made or implied by any figure, projection or scenario in these pages. Where funding requirements appear, they describe what executing the programme costs, not a proposition put to a reader.

Any relationship arising from this material would be established through separate, negotiated documentation, subject to the conditions precedent in Section IX and to the group's own readiness gates. Strictly internal; not for circulation.

SECTION I

Executive summary

TRL 3 ➡ 4

SYSTEM LEVEL · DERIVATION CLOSED · VALIDATION IN PROGRESS

The mathematical framework of the HST architecture is complete and internally consistent on paper: the architecture is well posed, which is not the same as the promised performance being demonstrated. On the strict definition, analytical proof-of-concept is TRL 3, and that is the level AURYNTHEON has demonstrated. Work is now under way in-house on a minimal-scale validation model: a laboratory-environment experiment whose completion is the TRL 4 criterion. The honest classification is therefore TRL 3 transitioning to TRL 4, level 3 achieved, level 4 in progress with a defined experiment rather than an intention. Stated this way it survives a formal readiness assessment, which a flat claim of TRL 4 would not until the measurements exist.

This dossier is an attempt to change the layer beneath artificial intelligence rather than the models that sit on top of it. It combines RRSC's internal due diligence on AURYNTHEON with a review of the scientific literature carried out with the support of AI tools. It must be stated clearly, because the document is for internal use and must reflect reality: an AI-assisted review is not an independent external validation. It is a qualitative orientation exercise, useful for framing the state of the art, but to be confirmed by an independent technical review from an accredited institution. Only that carries the weight of validation.

The thesis running through the document is a sharp distinction. The best known Western optimisations, Microsoft's BitNet b1.58, Mamba and the state-space family, MoE models, are software that squeezes more out of existing GPU hardware: a trajectory of evolution. AURYNTHEON instead proposes a reinvention, hardware and software co-designed from scratch, with a custom neuromorphic ASIC (C-SVA) that computes through sparse integer additions in an event-driven way, bypassing the von Neumann bottleneck.

The difference is the one between making a combustion engine more efficient and inventing the electric motor. It holds at the conceptual level, but it is still to be proven experimentally, and the credibility of the value case rests on saying so.

WHERE THE VALUE SITS

The asset is not a model. It is the computing layer a generation of models would run on. Whoever owns that layer owns an economic position with no natural ceiling: it is levied on every inference, in every jurisdiction, for the lifetime of the architecture. That is why the conditional upside is measured in tens of billions rather than in revenue multiples, and equally why the working valuation must stay anchored to the de-risked case until the silicon speaks.

THE SCALE OF THE CONDITIONAL CASE

If validated, AURYNTHEON would not merely improve AI, it would redefine the economic layer on which AI runs. The difference is between optimising the combustion engine and inventing the electric motor. Whoever controls that layer holds a position in every unit of inference the market consumes. The compute layer of AI is one of the most valuable positions in the global economy.

TWO QUESTIONS, KEPT SEPARATE

A dossier of this kind is read for two different things, and conflating them is the fastest way for a good asset to lose credibility. The first question is whether this group can be trusted to execute, answered by who the architect is, who the shareholders are, who has already partnered with us and on what terms. The second question is whether the technology works, answered only by training, a prototype and independent institutional review.

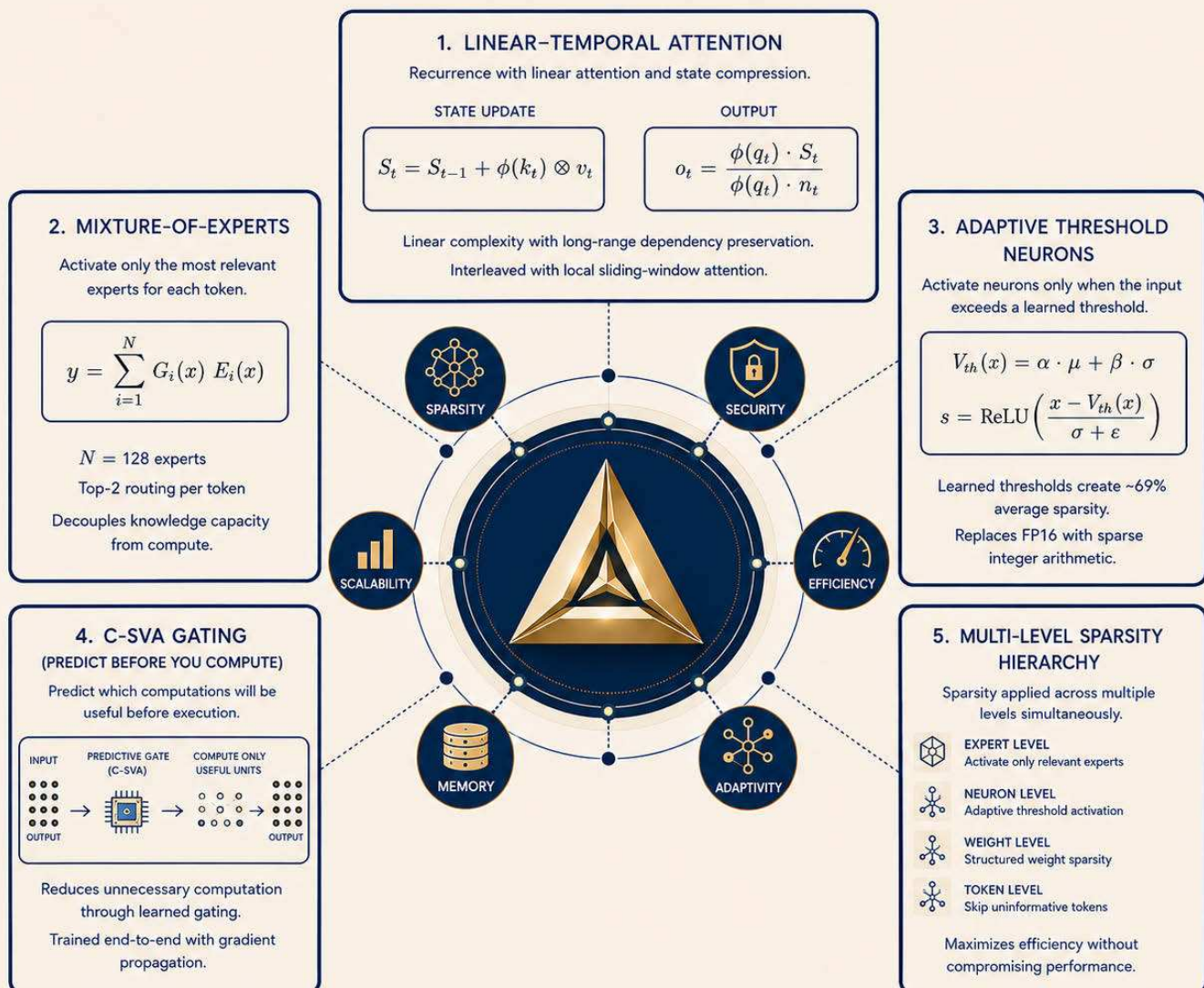
Every credential named in this document belongs to the first question. Colella's record at Google, Microsoft, Sony and Euroclear; the SecurityScorecard partnership; the Royal Diamond joint venture in the UAE with its operating branch entity; a clean cap table across an 18-jurisdiction footprint. These establish that the group is real, capitalised, governed and capable of delivering a programme of this size. They are strong. They are the reason the second question is worth pursuing at all. They are not evidence for it, and this document never presents them as such.

One consequence follows and is stated here because it governs how the whole dossier should be read: none of those parties is involved in AURYNTHEON. The architecture is developed by RRSC alone. Every third-party name in this document is a corporate reference, never a project reference. See Section VIII.

SUMMARY ASSESSMENT

DIMENSION	ASSESSMENT	RATING
Technical framework	O(n) + MoE + ATN + C-SVA: reinvention, not optimisation. Direction convergent with global science.	□□□□□
Energy reduction	The ~97% figure refers to consumption per single arithmetic operation versus dense. Partial on GPU, full only on a dedicated neuromorphic ASIC. Directionally plausible, to be measured.	□□□□□
Leadership (Colella)	Enterprise background across Google, Microsoft, Sony and Euroclear; top-level experience on national-security programmes whose details are covered by NDA and classification. Ab initio design. A credential of the team, not a validation of the design.	□□□□□
Market	A real AI energy crisis: data-centre pipelines in the tens of GW, sharply rising capex, capacity-price spikes.	□□□□□

DIMENSION	ASSESSMENT	RATING
Execution risk	Pre-silicon, TRL 3→4. The milestone-based institutional route systematically reduces risk by tying it to technical proof.	□□□□□
Alliance posture	Engagement restricted to NATO member states and allied jurisdictions. Code sovereignty; CFIUS-ready by construction; a Western sovereign-leaning asset.	□□□□□
Intellectual property	Strategy: secret architecture, not patenting. To be formalised: trade-secret regime, IP assignment from key personnel, confidentiality perimeter. An absolute precondition to investment.	□□□□□
Post-silicon value	Very high conditional value if the chip works. The working valuation must be anchored to the conservative de-risked case; the maximum figures remain hypotheses.	□□□□□



SECTION II

Evolution and reinvention

The Western approach, BitNet b1.58 (Microsoft), Mamba/SSM and MoE models, is predominantly software optimisation for existing hardware. These advances make Transformer architectures more efficient, replacing floating-point multiplications with integer additions (BitNet) or finding linear alternatives to quadratic attention (Mamba). It is a trajectory of evolution: getting the most out of the energy-hungry silicon we already have.

AURYNTHEON proposes a trajectory of reinvention. The goal is not to write better software for a GPU, but to design a completely new hardware and software system from the ground up. The HST architecture is meant for a custom neuromorphic ASIC (C-SVA) that performs computation through sparse integer additions in an event-driven way, bypassing the von Neumann bottleneck that afflicts every GPU.

The strength of the thesis is conceptual and it holds. Its limit is that, as of today, it remains a thesis: there is no prototype demonstrating the integrated system. The distinction between evolution and reinvention describes the project's ambition well, but does not replace experimental proof.

THE FRAMEWORK-FIRST ADVANTAGE, STATED PRECISELY

That the mathematical framework is complete and internally consistent before any hardware exists is a real and unusual advantage, and it should not be understated. Most programmes in this class proceed by empirical iteration at very large compute cost, discovering the architecture through experiment. AURYNTHEON enters the training phase with the derivation already closed, which is what makes a 7-billion-parameter proof-of-concept a meaningful test rather than a first guess: there is a specific predicted result to measure against.

What the completed framework is not is proof of performance. A closed derivation says the architecture is well posed and internally coherent; it does not establish that the predicted energy and scaling behaviour survive contact with real silicon, real data and real training dynamics. Those are the failure modes that no amount of analysis anticipates. The framework raises the prior. It does not settle the question, and the dossier's TRL 3 classification stands on exactly that distinction.

Convergence with neuromorphic research: how to read it properly

Several well-funded national research programmes outside the allied perimeter are advancing the same fundamental components as AURYNTHEON, and publishing peer-reviewed physical hardware in journals of record such as Nature Electronics. This fact must be read precisely, avoiding an inverted reading. The convergence confirms that the direction is scientifically reasonable. It does not confirm that AURYNTHEON works, nor that it is ahead. On the contrary: those labs already produce real, peer-reviewed physical components and subsystems, while AURYNTHEON has the integration framework and no prototype.

SECTION III

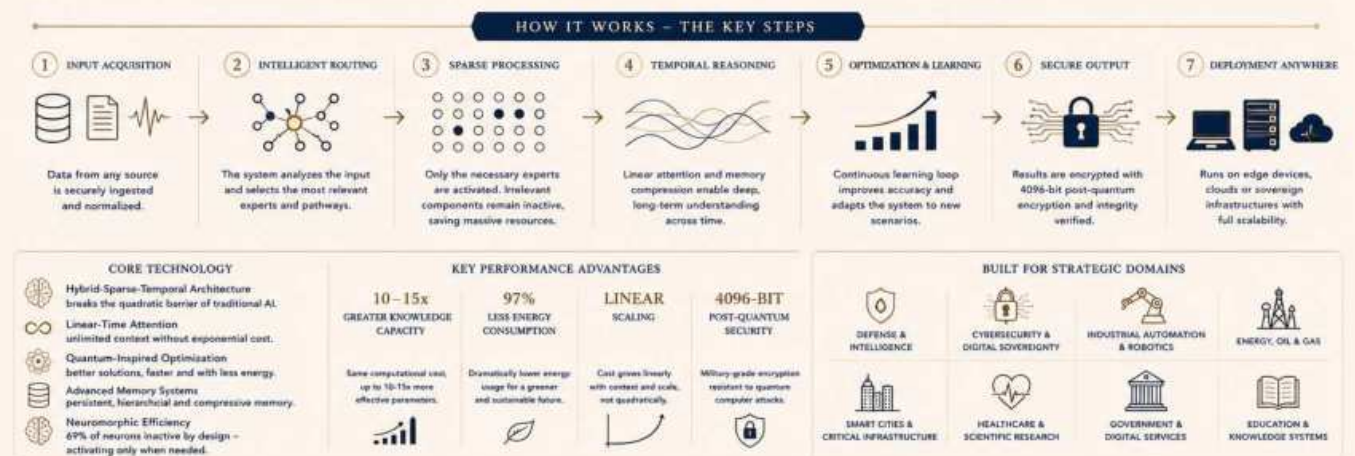
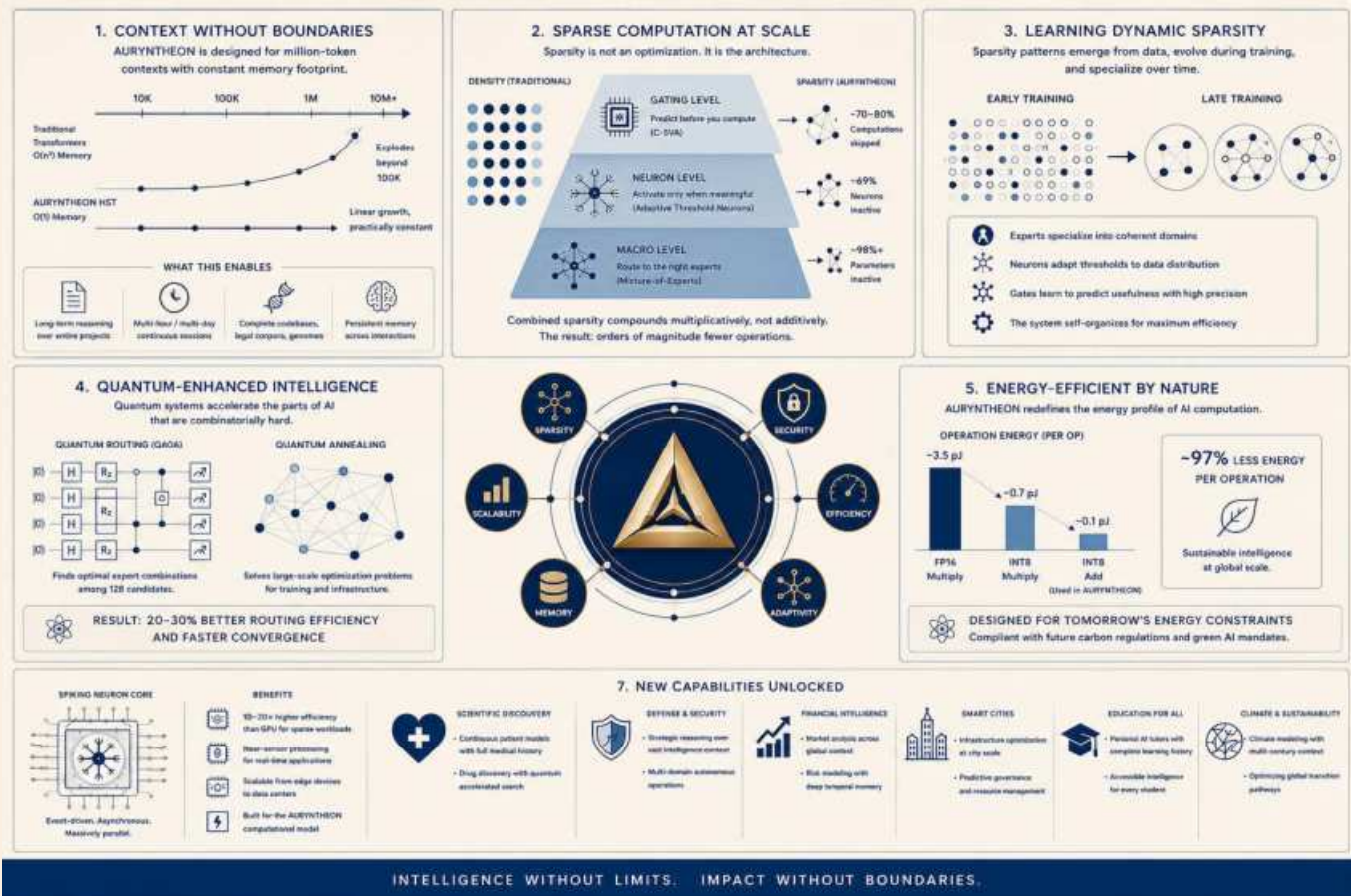
What follows if it works

- 1 The end of the scaling-law era. For five years the AI industry has been governed by a brutal truth: smarter models require exponential compute and energy. Hence the gigawatt data-centre crisis, a future in which only states can afford to train next-generation models. AURYNTHEON, with an efficiency gain of up to about 100,000x in the full neuromorphic scenario (a theoretical hypothesis, not demonstrated), would break this law, decoupling intelligence from energy consumption. The barrier to entry at the AI frontier would collapse from billions to millions. Every economic assumption currently priced into the sector rests on that barrier holding.
- 2 Freeing intelligence: edge AI becomes real. Today the most capable models live in vast data centres. The neuromorphic ASIC would let a model of GPT-6-class capability run on a smartphone, a drone, a satellite. Consequences: privacy, with the personal assistant entirely on-device and data never leaving your pocket; defence and autonomy, with drones and robots carrying real on-board intelligence and real-time decisions without a remote command link; ubiquity, with AI becoming ambient, cheap, everywhere. Each of those is a market in its own right.
- 3 A missing tier appears in the Western compute stack. The AI world runs on GPUs optimised for the matrix mathematics of Transformers, and for dense training, graphics and HPC that silicon remains the right answer. The C-SVA is silicon of a different nature, addressed at a different workload class: persistent inference, always-on sensing, embedded and edge autonomy, work the current architecture performs but performs expensively. If it works, the result is not one platform replacing another. It is a tier being added to the stack that the West does not currently have and that is being built elsewhere. That is the largest addition to the compute hierarchy since the accelerator itself.
- 4 The geopolitical chessboard tips over. The allied lead in AI rests on superior software and superior chip design. Competing state programmes are building their own hardware ecosystems and pursuing neuromorphic computing from the ground up. If AURYNTHEON works and is aligned with the West, it offers a sovereign AI capability that does not depend on the legacy GPU supply chain, a strategic asset of the first order, valued as such by defence and institutional counterparties rather than by commercial multiples.
- 5 A new class of intelligence. The efficiency gain is not only about speed and cost, but about capability. At that level of efficiency, models simply unthinkable today in terms of complexity would become possible. It is the most distant and most uncertain frontier among those described, and should be treated as such, but it is also the one that, if reached, makes every valuation in Section V look conservative.

Every scenario in the preceding section is conditional: it assumes that training and the prototype confirm the theory. At TRL 3 to 4 that is not yet the case. The value is real; the probability is not yet established. Both statements belong in the same document.

The market consequence

The consequence, if the chip is validated, is structural rather than incremental. The sector today is built on the assumption that intelligence is expensive to produce and expensive to operate. Every dependent structure, the hyperscaler oligopoly, the concentrated GPU supply chain, the centralisation of frontier capability in a handful of facilities, follows from that single assumption. An architecture that inverts it does not compete inside those structures; it removes the condition that produced them.



SECTION IV

Technical status and specification

BUILD STATUS · WHAT IS DONE AND WHAT IS MISSING

Hybrid-Sparse-Temporal architecture		DONE
Linear O(n) scaling		DONE
69% neuronal sparsity		DONE
4096-bit encryption		DONE
Temporal context memory		DONE
H/W-S/W interface spec		DEFINED
Training protocol blueprint		DEFINED
Minimal validation model		IN PROGRESS
7bn proof-of-concept · O(n) proof		TO DO
Full training and ASIC prototype		TO DO

Five components complete on paper. Two components, hardware-software interface and training protocol, fully defined. The minimal validation model is in progress in-house and is the experiment that carries the system to TRL 4. Full training and the ASIC prototype remain outstanding, and they are what the partner search is designed to unlock.

AURYNTHEON overcomes the fundamental limits of traditional AI through the Hybrid-Sparse-Temporal (HST) architecture, quantum-resistant security and extreme computational efficiency.

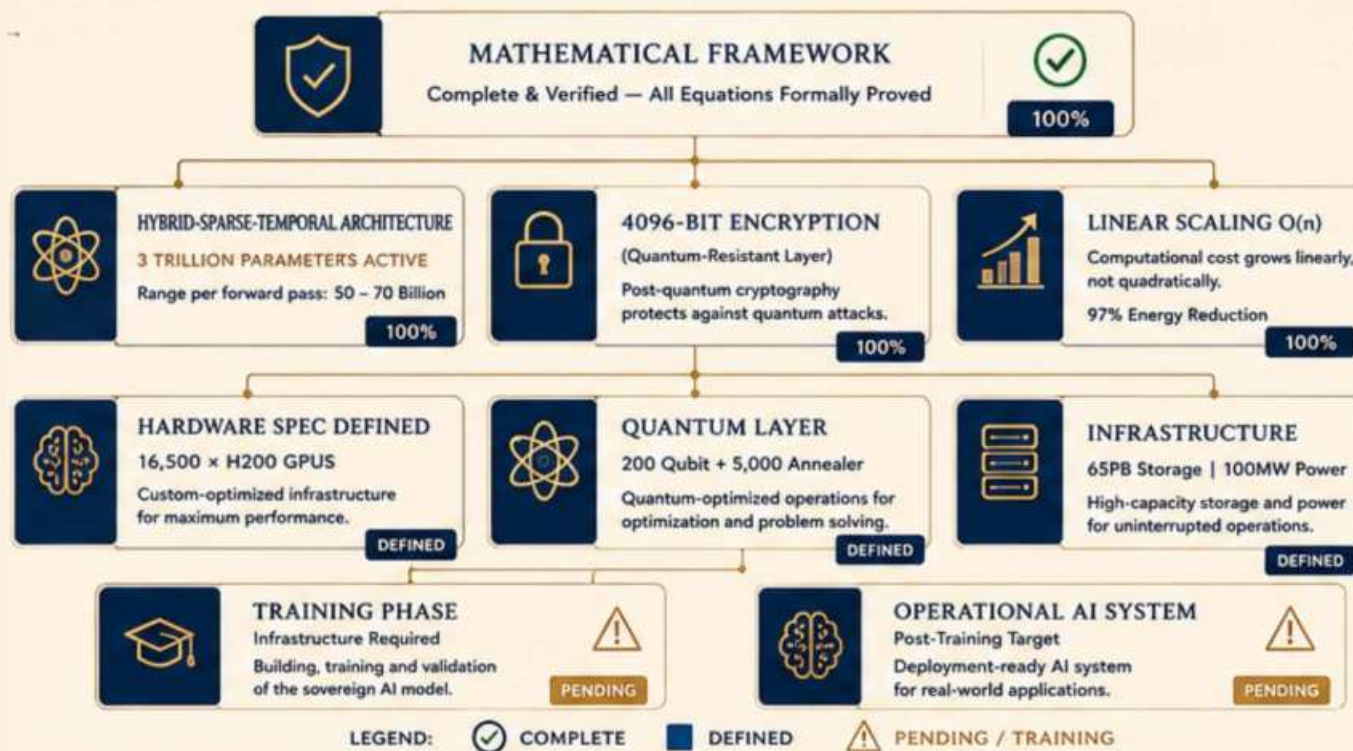


FIGURE 3 · DEVELOPMENT STATUS BY COMPONENT

Reconciliation note. The completion marks in this figure refer to the analytical work on each component, not to validated performance. In particular the ~97% energy reduction is a predicted figure and has not been measured on hardware; and formally proved here denotes a closed derivation, not machine-checked formal verification. System-level maturity remains TRL 3, per Section I and the chart above.

ARCHITECTURE SPECIFICATIONS (DESIGN)

PARAMETER	SPECIFICATION
Total parameters	3 trillion
Active parameters	50 to 70 billion per forward pass (69% neuronal sparsity)
Expert routing	Mixture-of-Experts, 128 experts, top-2 selection
Scaling complexity	linear $O(n)$ versus $O(n^2)$ for standard transformers
Energy reduction	~97% per single operation versus the dense equivalent
Encryption	4096-bit quantum-resistant (lattice-based post-quantum)
Training requirement	16,500 x H200 GPUs
Infrastructure	65 PB storage, 100 MW power envelope
Quantum layer	200 qubit plus 5,000 annealer, off critical path

SECTION V

Value and projections

B · REVENUE MODEL, FOUR STREAMS (YEAR 5)

STREAM	RRSC TARGET Y5	CONSERVATIVE	MARGIN	DRIVER / DEPENDENCY
Cloud API	€520m	€200 to 300m	70%+	Cost advantage per query; adoption speed
Enterprise licensing	€280m	€100 to 180m	80%+	Defence, finance, healthcare; €0.5 to 25m/year contracts
Neuromorphic hardware	€115m	€30 to 60m	40 to 50%	C-SVA chip sales; depends on ASIC fabrication
IP royalties	€35m	€10 to 25m	95%+	Third-party licences under a trade-secret and NDA regime
Total	€950m	€340 to 565m	72%	Conservative 340m; base 565m; RRSC target 950m

C · FIVE-YEAR PROJECTIONS, THREE SCENARIOS

PERIOD	RRSC	BASE	CONSERVATIVE	EBITDA
2027	0	0	0	-95m
2028	0	0	0	-125m
2029 launch	120m	90m	60m	-155m
2030	420m	280m	180m	+273m
2031	950m	565m	340m	+508m

HOW TO READ THE NUMBERS

The conservative column is the working base. The RRSC column is the ambitious target. The difference is not a detail: at TRL 3 the conservative case is the one on which to size expectations and phasing. The ambitious case materialises only by raising the TRL, with successful training and prototype.

D · VALUE FRAMEWORK BY TECHNICAL MILESTONE

STAGE	TECHNICAL TRIGGER	INDICATIVE VALUE OF THE POSITION	NATURE
SOFTWARE TRACK · VALUE OF THE OPERATING CAPABILITY, ASIC NOT ASSUMED			

STAGE	TECHNICAL TRIGGER	INDICATIVE VALUE OF THE POSITION	NATURE
Operating floor	Engineering capability monetised; no ASIC programme	ordinary service and licensing multiples	Going concern
Conservative	O(n) scaling demonstrated on standard hardware (70B)	€3.5bn	Working base
Base	500B model in production plus first enterprise contracts	€9.5bn	Software-track ceiling

THE SILICON LINE · BELOW IT A SOFTWARE COMPANY, ABOVE IT A SEMICONDUCTOR PLATFORM

Silicon validated	C-SVA tape-out; the ~97% energy figure measured on real hardware, pre-scale	€30 to 60bn	The inflection
Production	Production at scale; allied sovereign status	\$50 to 200bn	Conditional
Standard tier	Standard tier for event-driven compute; strategic integration at necessity pricing	€80 to 250bn	Capture dependent

VALUE CREATED AND VALUE CAPTURED

Value created and value captured are not the same number. The technology's economic significance runs into the trillions; RRSC's share is set by the deal structure, licensing, sovereign supply, co-development or integration, not by the physics. The figures above describe the position, not a proposition put to a reader, and they remain conditional on the validation ladder in Section IX.

NOT A BET WITH A CONSOLATION PRIZE

Track one, operating today. RRSC's teams write AI systems, models and production software from zero, for any sector. Revenue-generating now, independent of AURYNTHEON, and the same capability the neuromorphic stack requires. Technology-exchange only: sells capability and code without tying up capital in infrastructure.

Track two, the architectural option. The C-SVA, written on top of that business, funded in phases, exercisable only if the silicon performs. Capital is not underwriting survival, it buys the option while the underlying business keeps running. And a team that builds from first principles can keep originating new architectures.

SECTION VI

Competitive landscape

AURYNTHEON does not compete directly with existing models. It targets the underlying layer, the computing substrate. The more relevant comparison is not OpenAI versus Anthropic, but GPU versus neuromorphic ASIC.

PLAYER	ARCHITECTURE	STAGE	VALUATION	AURYNTHEON POSITION
OpenAI / GPT	Dense Transformer plus MoE	Production	\$300bn+	Cost advantage per query if the silicon is validated
Microsoft / BitNet b1.58	1.58-bit ternary LLM	Open-source	N/A	AURYNTHEON is a complete system; BitNet a component
Mamba / AI21 Jamba	SSM plus Transformer hybrid	Production	\$1.4bn	AURYNTHEON claims deeper hardware integration
Large open-weight MoE	671B MoE and comparable	Open-weight	N/A	Non-allied origin; allied-perimeter sovereignty is the differentiator
Intel Loihi 2	Edge neuromorphic ASIC	Research / edge	N/A	Edge only; AURYNTHEON targets cloud scale
SSI (Sutskever)	Undisclosed architecture	Pre-product	\$5 to 8bn	Most comparable stage; AURYNTHEON has more technical detail
NVIDIA (hardware)	H100/B200 GPUs for Transformers	Leader	over \$1,000bn	Complementary tier, not a competitor: optimal for dense training and HPC. The most probable integrator or acquirer of this architecture, not its target.

THE NEXT GENERATION OF SOVEREIGN ARTIFICIAL INTELLIGENCE

A sovereign AI architecture engineered to address the world's most complex challenges with intelligence, precision and responsibility.



Strategic Application Domains

AURYNTHEON has been conceived as a unified cognitive architecture capable of supporting multiple strategic sectors through a single mathematical and computational foundation. Rather than developing independent AI systems for each industry, the architecture applies the same Hybrid-Sparse-Temporal (HST) principles to different operational environments, providing scalable intelligence, computational efficiency and long-context reasoning across critical domains.

In the field of Defense & Strategic Intelligence, AURYNTHEON enables persistent operational awareness by processing vast volumes of heterogeneous information while maintaining computational efficiency. Its architecture supports long-horizon reasoning, multi-domain information fusion, autonomous mission planning and decision support without the exponential computational growth associated with conventional dense transformer models.

Within Cybersecurity, AURYNTHEON is designed to analyse continuously evolving digital environments through selective computation. By activating only the computational resources required for each task, the architecture reduces unnecessary processing while improving scalability for threat detection, infrastructure protection, cyber defence and intelligent security operations across complex digital ecosystems.

For Industrial Automation & Robotics, the architecture allows intelligent coordination of robotic systems, predictive maintenance and production optimisation through dynamic expert specialisation. Instead of processing every industrial variable equally, AURYNTHEON allocates computational resources only where meaningful decisions are required, improving operational efficiency while supporting increasingly complex manufacturing environments.

In the Energy, Oil & Gas sector, AURYNTHEON provides a computational framework for monitoring critical infrastructure, analysing operational conditions and optimising industrial processes over extended time horizons. Its ability to maintain long contextual memory enables continuous infrastructure intelligence while significantly reducing computational cost compared with traditional dense architectures.

For Smart Cities & Critical Infrastructure, AURYNTHEON integrates information originating from transportation systems, utilities, communications networks and public infrastructure into a unified decision-support framework. The architecture is designed to maintain situational awareness across interconnected urban systems while efficiently scaling to millions of continuously evolving data sources.

Within Healthcare & Scientific Research, AURYNTHEON supports the analysis of large medical datasets, scientific modelling and long-term research programmes requiring extended contextual understanding. The architecture combines high knowledge capacity with computational efficiency, enabling advanced analytical workloads that become progressively more practical as computational costs are reduced.

In Government & Digital Sovereignty, AURYNTHEON provides a sovereign artificial intelligence architecture capable of supporting secure digital public services, strategic decision support and large-scale institutional knowledge management. The platform has been conceived to operate within national infrastructures while maintaining efficiency, scalability and technological independence.

Together, these application domains demonstrate that AURYNTHEON is not a collection of specialised AI products but a single architectural foundation capable of adapting to multiple strategic environments. By combining Hybrid-Sparse-Temporal computation, expert specialisation, adaptive activation and long-context reasoning within one unified framework, AURYNTHEON establishes a scalable cognitive platform designed to address the computational, operational and strategic challenges of next-generation artificial intelligence.

SECTION VIII

Corporate structure and governance

RRSC is a Delaware LLC operating in over 18 nations. AURYNTHEON is positioned as an allied sovereign asset, with full IP control retained by RRSC. The items below answer one question and one question only: whether this group is credible, governed and capable of carrying a programme of this scale. None of them speaks to whether the architecture works, that is settled in Sections IV and IX.

PROGRAMME OWNERSHIP AND EXECUTION

All work on AURYNTHEON is carried out by R. Rockefeller S.C.'s own technical team, led by Umberto Colella, under the direct supervision and management of Valentino Loforese, Chief Executive Officer and majority shareholder. The architecture, the mathematical framework, the hardware and software specifications and the training protocol are the product of that team and of no one else.

Reporting and decision authority run in a single line from the technical lead to the CEO. There is no external technical direction, no outsourced development, no consortium and no steering committee outside the group. The programme is under RRSC's exclusive control at every level: technical, financial and strategic.

THIRD-PARTY RELATIONSHIPS AND THE AURYNTHEON PERIMETER

No current partner, counterparty, joint-venture participant, advisor, cybersecurity provider or other third party of R. Rockefeller S.C. is involved in AURYNTHEON in any capacity. Not as a contributor, not as a co-developer, not as a licensee, and not as a holder of any right, option, participation or claim over the architecture or its intellectual property.

No partner is a guarantor, sponsor, endorser or referee of this programme, and no partner relationship may be cited, by the group or by any reader of this dossier, as validation, corroboration or supporting analysis for AURYNTHEON. To take the clearest example: SecurityScorecard is a strategic partner of R. Rockefeller S.C. in respect of the company's own services and security posture. It guarantees nothing whatsoever in relation to this project, has no visibility of it, and its partnership must never be presented as though it did. The same holds for every other name in this section without exception.

Every third-party relationship recorded in this section, the UAE joint venture, the cybersecurity partnership, the advisory network, the officers of the UAE branch and their professional histories, is a corporate reference for R. Rockefeller S.C. as an operating group. Each establishes that the group is real, governed, capitalised and capable of executing at scale. None is a reference for AURYNTHEON. The programme is developed exclusively in-house, by RRSC's own teams, and the training and development partner referred to throughout is still under selection.

This is a governance strength, not a limitation, and it should be presented as one. Because no third party has contributed to the architecture, the IP provenance is a single unbroken chain from the lead architect to RRSC. That is exactly the position a CFIUS review, an accredited technical reviewer and any eventual

acquirer want to find, and it is far rarer than it sounds.

CRITICAL GOVERNANCE ITEM · IP ASSIGNMENT

IP assignment is not an optional feature of this structure. The architecture is the asset.

HST ARCHITECTURE
INTELLIGENCE THAT PROTECTS

AURYNTHEON integrates 4096-bit cryptography with post-quantum algorithms to protect data, communications and infrastructures against both current and future attacks, including quantum computers.

WE ARE POST-QUANTUM.

THE QUANTUM THREAT

Quantum computers will be able to solve the mathematical problems that protect today's encryption.

RSA, ECC and other classical algorithms will become vulnerable.

The time to act is now.

Shor's Algorithm Breaks RSA & ECC

$N = P \times Q$

$\log N$

OUR SOLUTION: 4096-BIT POST-QUANTUM ENCRYPTION

- 4096-BIT KEY SIZE**
Extreme cryptographic strength for maximum security and long-term protection.
- POST-QUANTUM ALGORITHMS**
Based on lattice-based cryptography, resistant to attacks from quantum computers.
- HYBRID SECURITY MODEL**
Combination of classical and post-quantum algorithms for a secure transition to the quantum-safe era.
- INTEGRATED IN AURYNTHEON HST**
Optimized, scalable and high-performance encryption inside our sovereign AI architecture.

CLASSICAL CRYPTOGRAPHY vs QUANTUM	
CLASSICAL ALGORITHMS (TODAY)	QUANTUM COMPUTERS (FUTURE)
RSA / ECC	→ VULNERABLE
2048-BIT KEYS	→ INSUFFICIENT
MATHEMATICAL PROBLEMS (FACTORIZATION, DISCRETE LOG)	→ SOLVABLE BY QUANTUM ALGORITHMS (SHOR)
AT RISK	→ COMPROMISED

WHY 4096-BIT?

- Exponentially stronger than 2048-bit.
- Designed for a future where quantum power is reality.
- Protects classified data, critical infrastructures and sovereign communications.
- Ensures confidentiality, integrity and authenticity for decades to come.

PROTECTED APPLICATIONS

- GOVERNMENT & DEFENSE
- SECURE COMMUNICATIONS
- CRITICAL INFRASTRUCTURES
- CLOUD & DATA CENTERS
- HEALTHCARE & RESEARCH

OUR COMMITMENT
We build the security of today for the world of tomorrow.
AURYNTHEON.
INTELLIGENCE. SECURITY. SOVEREIGNTY.

4096-BIT SECURITY
Extreme key size for maximum protection

POST-QUANTUM RESISTANT
Secure against future quantum attacks

HYBRID DEFENSE
Classical + Post-Quantum for a secure transition

SOVEREIGN ARCHITECTURE
Full control. No backdoors. No dependencies.

BUILT FOR THE FUTURE
Future-proof encryption for generations.

AURYNTHEON
INTELLIGENCE THAT SERVES.
TECHNOLOGY THAT PROTECTS.

THE ROB ROCKEFELLER STANDARD CARBON LLC

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AGS
GEOTHERMAL
SOLUTIONS

AI
SOFTWARE
DEVELOPMENT

**GEOTHERMAL
ENERGY**

SUSTAINABILITY

**CYBER
SECURITY**

**GLOBAL
STRATEGY**

R. ROCKEFELLER S.C.

AI